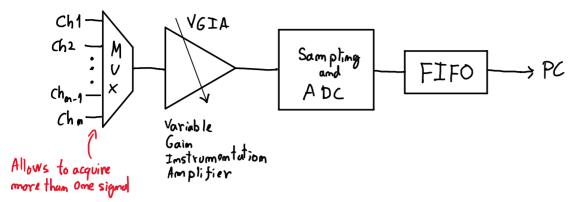
1a) Draw the circuit diagram of a data acquisition card (DAQ), which is the component that allows you to acquire more than one signal, with a single ADC? Does the acquisition of multiple signals occur simultaneously? Justify your answers.



## Those are not acquired simultaneosly, but squantialy controlled by a clock

1b) Indicate what minimum characteristics (input mode, ADC frequency, single channel gain, number of bits) a DAQ must have, inside which there is an amplifier with gains  $G_i = 0.5, 1, 10, 100$  and an A/D converter with dynamics  $\pm 3$  V, to be able to simultaneously acquire the following signals:

 $V_1$  Analog signal with a maximum bandwidth of 20 kHz, maximum amplitude 100 mV peak-to-peak, with zero average value, of which you want to appreciate details with a resolution better than 0.1 mV.

 $V_2$  Analog square wave with levels 0 V and 3 V, at a frequency of 500 Hz, of which at least 50 samples per period must be acquired.

 $V_3$  Temperature signal from a thermocouple, with sensitivity of 50  $\mu$ V/K, used to measure a temperature of an oven around 500 °C, with a resolution of at least 0.35 °C.

 $V_4$  Signal from an absolute pressure sensor, with sensitivity of 15 mV/kPa placed 10 meters below sea level (1 atm = 101.325 kPa) that measures the pressure with resolution  $\Delta V_4 = 1$  mV.

$$G_1 = 0.5 j \cdot 1 j \cdot 10 j \cdot 100$$
  $D = 13 V$ 
 $V_1$ :

 $B_W = 20 k H_z \Rightarrow f_5 = 40 k H_z$ 
 $D = \frac{1}{2} \cdot 50 \text{ mV}$   $D C = 0$ 
 $G = 10 \Rightarrow D = \frac{1}{2} \cdot 0.5 V$ 
 $\Delta V = 0.1 \text{ mV}$ 
 $G_2^m \geqslant \log_2 \left( \frac{6}{\Delta V \cdot G} \right) \Rightarrow m = 13$ 

$$V_2$$
:  
 $D=+3 V \Rightarrow G=1$   
 $f=500Hz$  with  $505/pariod$   
 $f_s=25KHz$ 

$$V_2: f_5 = 25 \text{ kHz}$$
  
 $G = 1$   
 $m = -$ 

$$V_4$$
:  $f_s = G = 1$ 
 $M = 13$ 

8 chammels im Differential mode 13 bits and  $f_5 = 160 \text{ KHz}$ 

$$T = 500 \Rightarrow V = S \cdot (T - T_{AMB}) = 23.75 \text{ and}$$
  
 $G = 100$ 

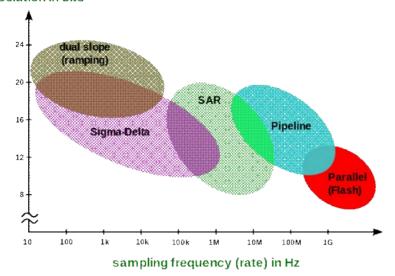
$$\Delta T = 0.35$$
  $C^{\circ}$   
 $C > 2^{\circ} > \log_2 \left( \frac{6}{6 \cdot \Delta T \cdot 5} \right) = > m = 12$ 

P = phg + 1atm = (98 + 101.3) kPa = 199.3 kPa  
V = P.5 = 2.99 => 6 = 1  

$$2^{m} > \log_{2}(\frac{6}{\Delta V_{4} \cdot 6}) => M = 13$$

1c) Which type of analog-to-digital converter is probably used in the DAQ, as it is suitable for this measurement? If it were necessary to acquire an additional V5 signal with 10 MHz analog bandwidth and amplitude that varies between  $\pm 2$  V, which type of converter (indicate the architecture among those known) would be necessary to use inside a new acquisition card, which simultaneously acquires all five signals?

## resolution in bits



13 bit and 160 KHZ Co Can either be a sigma delta or a SAR  $V_S \Rightarrow f_s = 20 \text{ MHz}$ Told  $f_S = 100 \text{ MHz} = > \text{Pipe line}$