

1) Fig. 1 shows a closed-loop buck converter with current-programmed mode (CPM) controller (peak current-mode). No compensation ramp is employed. The op-amp and the converter can be considered ideal. Consider the following specifications: $V_{in} = 12\text{ V}$, $V_{ref} = 5\text{ V}$, $C = 100\text{ }\mu\text{F}$, $L = 4\text{ }\mu\text{H}$, $f_s = 200\text{ kHz}$, $I_o = V_o/R = 20\text{ A}$, $R_f = 0.1\text{ }\Omega$.

a) Calculate the steady-state duty cycle, D .

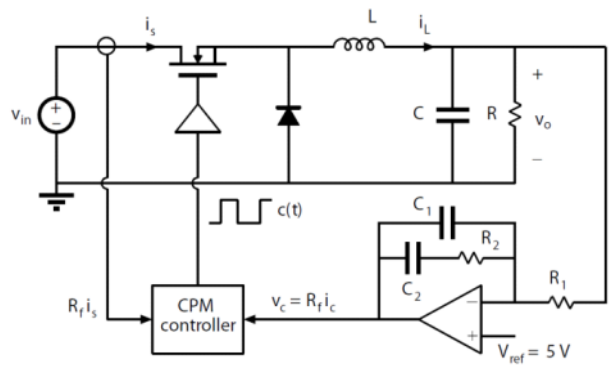
b) Derive the transfer function $G_{oc}(s) = \frac{V_o}{V_c}$ (hint: consider a simple first-order model where the inductor is replaced by a VCCS). Sketch the Bode plot of $|G_{oc}(j\omega)|$. An open-loop transfer function, $L(s)$ is desired, having a pole in the origin, a second pole at $f_{p2} = 50\text{ kHz}$ and a phase-margin, $\phi_m = 68^\circ$.

c) Calculate the crossover frequency, f_c .

d) Derive the compensator transfer function, $G_c(s)$. Sketch the Bode plot of $|G_c(j\omega)|$.

e) Given $R_1 = 10\text{ k}\Omega$, design the compensator, i.e., select R_2 , C_1 , and C_2 to meet the design specifications.

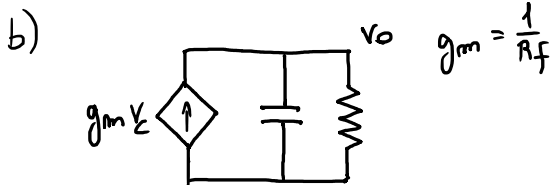
f) Sketch the asymptotic plot of the closed-loop output impedance.



$$1) (V_{in} - V_o)D - V_o(1-D) = 0 \Leftrightarrow \frac{V_o}{V_{in}} = D$$

$$V_o = V_{ref} = 5\text{ V} \Rightarrow \frac{5}{12} = D = 0.417$$

$$V_{in} = 12\text{ V}$$



$$V_o = g_m V_c \cdot (R // C)$$

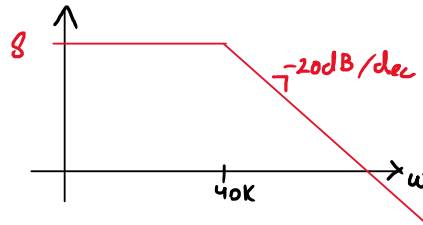
$$(R // C) = \left(\frac{1}{R} + j\omega C \right)^{-1} = \frac{R}{1 + j\omega CR}$$

$$G_{oc} = \frac{V_o}{V_c} = \frac{R}{R_f} \cdot \frac{1}{1 + j\omega CR}$$

$$|G_{oc}(0)| = \frac{R}{R_f} = 2.5 \Rightarrow 7.96\text{ dB}$$

$$\omega_p = \frac{1}{CR} = 40\text{ Krad/s}$$

$$\hookrightarrow 6.37\text{ kHz}$$



c)

$$L(s) = G_c(s) \cdot G_{oc}(s)$$

$$\phi_m = 68^\circ = 1.1868\text{ rad}$$

$$L(s) = \frac{K}{s} \cdot \frac{1}{1 + s/\omega_{p2}}$$

$$\phi_m = 180 + \angle[L(j\omega_c)] = -\arctan\left(\frac{\omega}{\omega_p}\right) - \frac{\pi}{2}$$

↗ phase from pole at origin

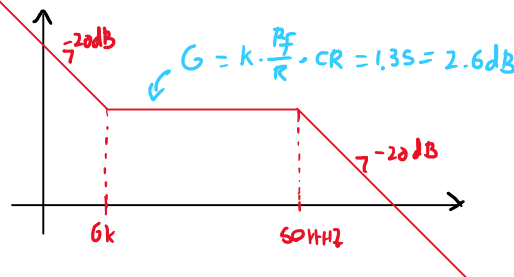
$$\tan\left(-\phi_m - \frac{\pi}{2}\right) \cdot \omega_{p2} = \omega = 20.2\text{ kHz}$$

d)

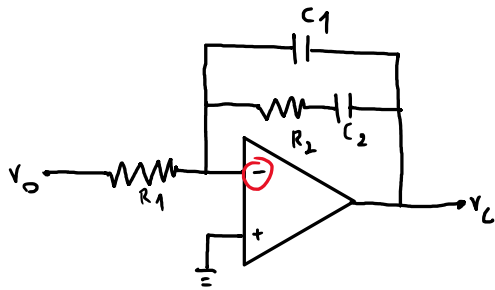
$$1 = \left| \frac{K}{s} \cdot \frac{1}{1 + s/\omega_{p2}} \right|$$

$$1 = \frac{|K|}{|j\omega_o - \frac{\omega_o^2}{\omega_p}|} \Rightarrow K = \omega_o \sqrt{1 + \frac{\omega_o^2}{\omega_p^2}} = 136 \times 10^3 = 1.36 \times 10^5 = 102.7\text{ dB}$$

$$\frac{K}{s} \cdot \frac{1}{1 + s/\omega_{p2}} = \frac{R}{R_f} \cdot \frac{1}{1 + j\omega CR} \cdot G_c \Leftrightarrow G_c = K \cdot \frac{R_f}{R} \cdot \frac{1}{s} \cdot \frac{(1 + j\omega CR)}{(1 + s/\omega_{p2})}$$



e)



$$\frac{V_o}{V_L} = -G_C = -\frac{1}{\Delta C_2 R_1} \cdot \frac{1 + \Delta R_2 C_2}{1 + \Delta R_2 C_1}$$

$$G_C = K \cdot \frac{R_f}{R} \cdot \frac{1}{\Delta} \cdot \frac{(1 + \Delta C R)}{(1 + \Delta R_2 C_1)}$$

$$\frac{1}{C_2 R_1} = K \cdot \frac{R_f}{R} \Rightarrow C_2 = \frac{1}{R_1} \cdot \frac{1}{K} \cdot \frac{R}{R_f} = 1.85 \text{ mF}$$

$$R_2 C_2 = C R \Rightarrow R_2 = \frac{C R}{C_2} = 13.5 \text{ k}\Omega$$

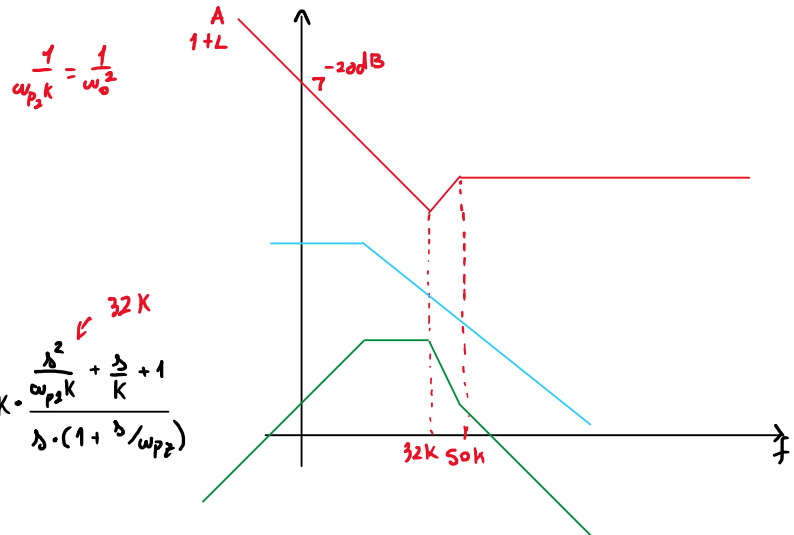
$$\frac{1}{\omega_{p_3}} = R_2 C_1 \Rightarrow C_1 = \frac{1}{\omega_{p_3} R_2} = 236 \text{ pF}$$

f) $Z_{oCL} = \frac{Z_{oOL}}{1 + L(s)}$

$$Z_{oOL} = \frac{R}{1 + \Delta C R}$$

$$A = 1 + L = 1 + \frac{K}{\Delta} \cdot \frac{1}{1 + \Delta / \omega_{p_2}}$$

$$\frac{\Delta \cdot (1 + \Delta / \omega_{p_2}) + K}{\Delta \cdot (1 + \Delta / \omega_{p_2})} = \frac{\frac{\Delta^2}{\omega_{p_2}} + \Delta + K}{\Delta \cdot (1 + \Delta / \omega_{p_2})} = K \cdot \frac{\frac{\Delta^2}{\omega_{p_2} K} + \frac{\Delta}{K} + 1}{\Delta \cdot (1 + \Delta / \omega_{p_2})}$$

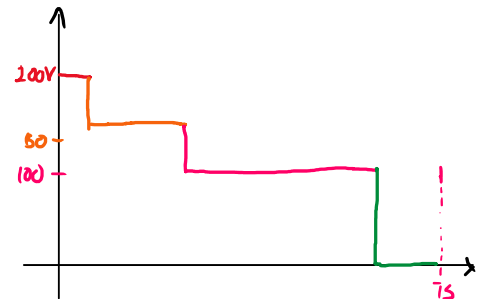


The diagram shows a full-bridge inverter circuit. The inverter consists of four MOSFETs (Q_1, Q_2, Q_3, Q_4) and four diodes (D_1, D_2, D_3, D_4) arranged in a bridge configuration. The input voltage V_{in} is applied to the gates of the MOSFETs. The output of the inverter is connected to the primary winding of a transformer with a turns ratio of $1:n$. The secondary winding is connected to a load consisting of a capacitor C and a resistor R in parallel. The output voltage V_o is measured across the load.

- 2) ON
-
- OFF
-
- at L_m : $V_{im} D - \frac{V_O}{m} (1-D) = 0 \Rightarrow D = \frac{1}{3} \quad \frac{V_O}{V_{im}} = \frac{D}{1-D} m$
- $I_{Lmb} = \frac{\Delta I}{2} = \frac{V_{im} D}{2 L_m f_s} = \frac{V_O (1-D)}{2 L_m f_s} \cdot \frac{1}{m}$
- $I_o \text{ in CCM} = I_{im} \cdot \frac{1-D}{D} \cdot \frac{1}{m} \Leftrightarrow \frac{V_O}{R} = \bar{I}_L (1-D) \cdot \frac{1}{m} \Leftrightarrow \bar{I}_L = \frac{V_O}{(1-D) R} \cdot m$
- $\bar{I}_L = \frac{I_{im}}{D}$
- $\bar{I}_L < I_{Lmb} \Leftrightarrow \frac{I_o m}{(1-D)} < \frac{V_O (1-D)}{2 L_m f_s} \cdot \frac{1}{m} \Leftrightarrow$
- $\Leftrightarrow I_o < \frac{V_O (1-D)^2}{2 L_m f_s} \cdot \frac{1}{m^2} \Leftrightarrow P_o < \frac{V_O^2 (1-D)^2}{2 L_m f_s} \cdot \frac{1}{m^2}$
- b) $P_o = \frac{V_O^2}{R} = \frac{1}{2} L_m \Delta I^2 f_s$
- $\hookrightarrow 100 < 1000 \text{ W! It's DCM!}$

$$\Delta I = \frac{V_{IN} D}{L_m f_s} \quad \frac{V_o^2}{R} = \frac{1}{2} L_m \left(\frac{V_{IN} D}{L_m f_s} \right)^2 f_s \Leftrightarrow \frac{V_o}{V_{IN}} = D \sqrt{\frac{R T_s}{2 L_m}} \quad ! \quad D = 0.1 !$$

- (1)
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- The circuit diagram shows a full-wave bridge rectifier. The input voltage v_{im} is applied across the bridge. The output voltage v_o is taken from the bridge terminals. The load resistor R_L is connected in series with the output. The current through the load is i_L . The average output voltage is given as $V_{Lm} = \frac{2}{\pi} V_m$.



②

150V

$V_{Lm} = \frac{V_p}{5}$

150V

③

100V
10
100V

$$d) \quad v_{im} - \frac{v_0}{m} = L \frac{\Delta I}{\Delta t} \quad \rightarrow \Delta t = 100 \text{ ns}$$

$$\Delta I \approx \frac{v_{im} D}{L_m f_S}$$

c) Reduce core losses: $\hat{B} \propto \frac{1}{\rho}$

Time $L_m : L_m \propto m$